



PATENTS
ALT-153 Cont. 2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicants : Christopher F. Lane et al.
Application No. : 10/047,468 Confirmation No. : 3164
Filed : January 14, 2002
For : FAST SIGNAL CONDUCTOR NETWORKS FOR
PROGRAMMABLE LOGIC DEVICES
Group Art Unit : 2819
Examiner : Don P. Le

New York, New York 10020
June 28, 2004

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

REPLY TO OFFICE ACTION

Sir:

Responsive to the Office Action dated April 19, 2004
in the above-identified application, consideration of the
following is requested.

Claims 1-13 were rejected under the judicially created
doctrine of obviousness-type double patenting as being
unpatentable over claims 1-32 of U.S. patent 6,373,280.

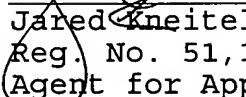
Accordingly, attached hereto is a Terminal Disclaimer
disclaiming the portion of the term of any patent to be granted

on this application which would extend beyond the expiration of
U.S. Patent 6,373,280.

A check in the amount of \$110.00 in payment of the fee
for filing the Terminal Disclaimer Under 37 C.F.R. § 1.321 (b,c)
is enclosed.

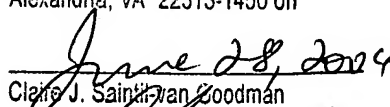
In view of the Terminal Disclaimer, the judicially
created doctrine of double patenting rejection of claims 1-13
has been overcome. Reconsideration and allowance of this
application are respectfully requested.

Respectfully submitted,

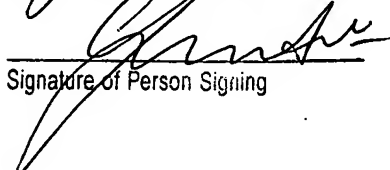


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